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Substitute for form 1449/PTO

Under the Peperwork Reduction Act of 1855 Sep

Application Number 10/781,166

Filing Date February 18, 2004 First Named Inventor Richard W. Foote Art Unit 2829

Not Yet Assigned

STATEMENT BY APPLICANT (Use as many shoots as necessary)

INFORMATION DISCLOSURE

Examiner Name Attorney Docket Number 1 P05810

Examiner	Cite	Document Number	Publication Date	T DOCUMENTS Name of Patentee or	Pages, Columns, Lines, Where
initials*	\$ 2	Number-Kind Code ^{2 (Finance)}	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many choots as necessary)

First Named Inventor Richard W. Foote 2829 **Examiner Name** Not Yet Assigned Attorney Docket Number | P05810

Examiner Initiate*	Cite No.	Document Number Number-Kind Code ^{2 (Finom)}	Publication Date MM-DD-YYYY		ENTS Name of Patantee or opticant of Cited Document	Peges, Columns, Lines, Wh Relevent Passeges or Relev Figures Appear	rent
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INF	ORMATION	1 DIS	SCLOSURE	Filing Date	February 18, 2004			
STATEMENT BY APPLICANT			PPLICANT	First Named Inventor	Richard W. Foote			
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Sheet	3	of	3	Attorney Docket Number	P05810			

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Examiner Initiats*	Cite No.1	NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, senal, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
TNIC		Simon S. Cohen et al., "Laser-Induced Melting of Thin Conducting Films: Part I-The Adiabatic Approximation", IEEE Transactions on Electron Devices, Vol. 38, No. 9, September 1991, Pp. 2042-2050.			
	СВ	Joseph B. Bernstein et al., "Laser Energy Limitation for Buried Metal Cuts" IEEE Electron Device Letters, Vol. 19, No 1, January 1998, Pp. 4-6.			
	СС	Simon S. Cohen et al., *Laser-Induced Melting of Thin Conducting Films: Part II-Heat-Dissipating Substrates*, IEEE Transactions on Electron Devices, Vol. 38, No. 9, September 1991, Pp. 2051-2057.			
	CD	Will R. Moore, "A Review of Fault-Tolerant Techniques for the Enhancement of Integrated Circuit Yield", Proceedings of the IEEE, Vol. 74, No. 5, May 1986, Pp. 684-698.			
	CE	Yunlong Sun et al., "Optimization of Memory Redundancy Laser Link Processing", SPIE Vol. 2636, Rp. 152-164.			
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	CG	Ronald P. Cenker et al., "A Fault-Tolerant 64K Dynamic Random-Access Memory", IEEE Transactions on Electron Devices, Vol. ED-26, No. 6, June 1979, Pp. 853-860.			
	СН	A. S. Tenney et al., "Etch Rates of Doped Oxides in Solutions of Buffered HF", J. Electrochem Soc.: Solid-State Science and Technology, August 1973, Pp. 1091-1095.			
V	CI	Gang Yang, "Laser Energy Window Simulation for Metal Cut Structure" Thesis for the Degree of Master of Science, University of Maryland, 1999, 104 pages.			
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